

#4/Election

Mueish

FAX COPY RECEIVED

SEP 20 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

TECHNOLOGY CENTER 2800

In re Application: Brown et al.

Today's Date: September 18, 2002

Serial No.: 10/064,301 ✓

Group Art Unit: 2812

Filed: July 1, 2002

Examiner: Tsai, H Jey

Title: METHOD AND STRUCTURE FOR SCALABLE, LOW-COST POLYSILICON CAPACITOR IN PLANAR DRAM

RESPONSE TO RESTRICTION REQUIREMENT

To the Assistant Commissioner of Patents
Washington, D.C. 20231

Dear Sir:

Responsive to the Office Action mailed September 11, 2002, and the restriction requirement made therein, Applicants elect, without traverse, the subject matter of Group I, claims 1-10, for continued examination herein. This election is made without prejudice, and with the understanding that Applicants reserve the right to file further applications based on the unelected claims.

Respectfully submitted,

For: Brown et al.

By: Mark F. Chadurjian

Mark F. Chadurjian

Reg. No. 30,739

Telephone: (802) 769-8843

Facsimile (802) 769-8938

Email: mchadurj@us.ibm.com

IBM Corporation, IPLaw Dept. 972E
1000 River Street
Essex Junction, VT 05452

CERTIFICATE OF MAILING

I hereby certify that, on the date shown below, this correspondence is being:

MAIL

☐ deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, DC 20231.Date: 9/20/02

FACSIMILE

☒ transmitted by facsimile to the Patent and Trademark Office.Maryann Luisi
Name
Maryann Luisi
Signature

BUR920010185US1

1

S/N 10/064,301